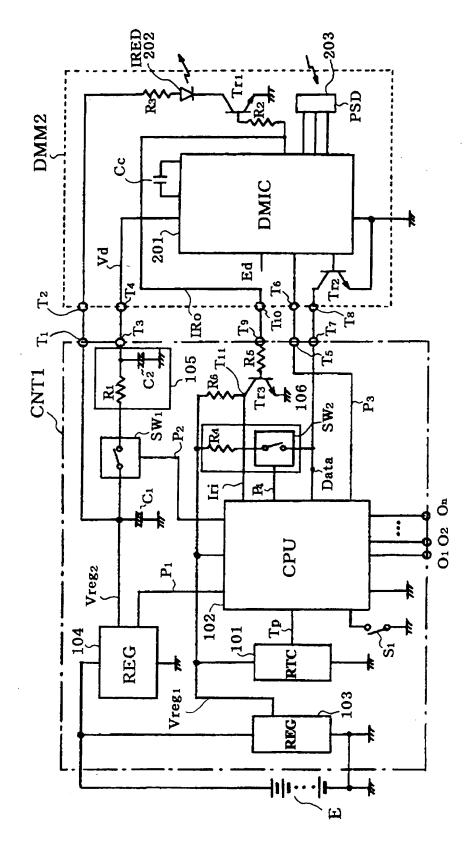
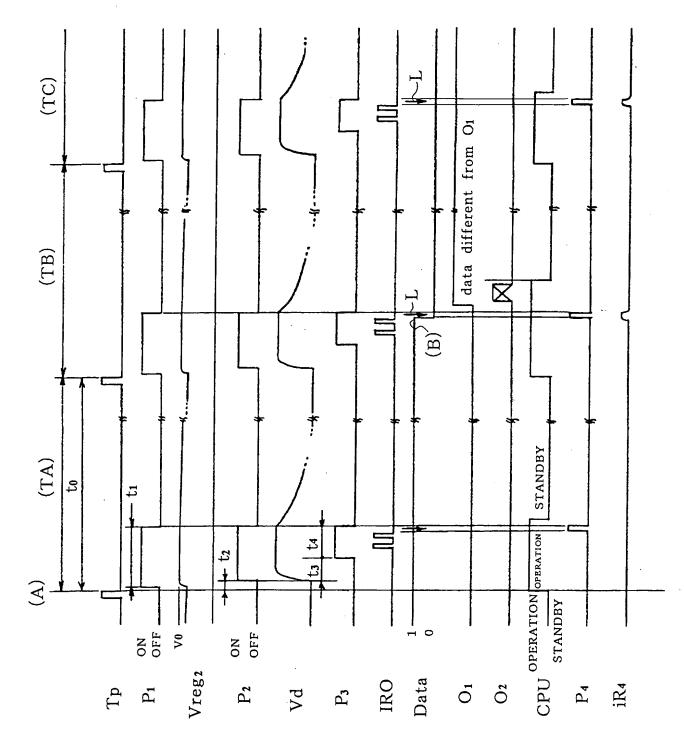
FIG. 1







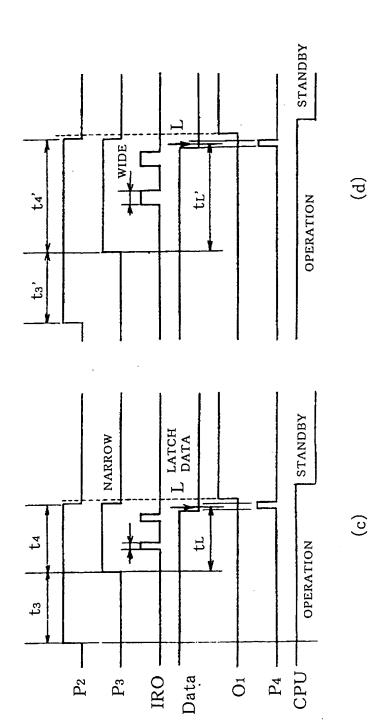


FIG. 4

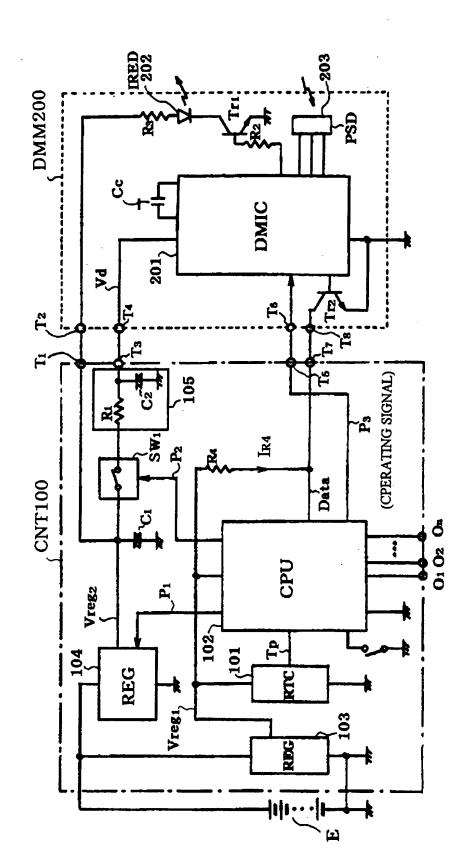
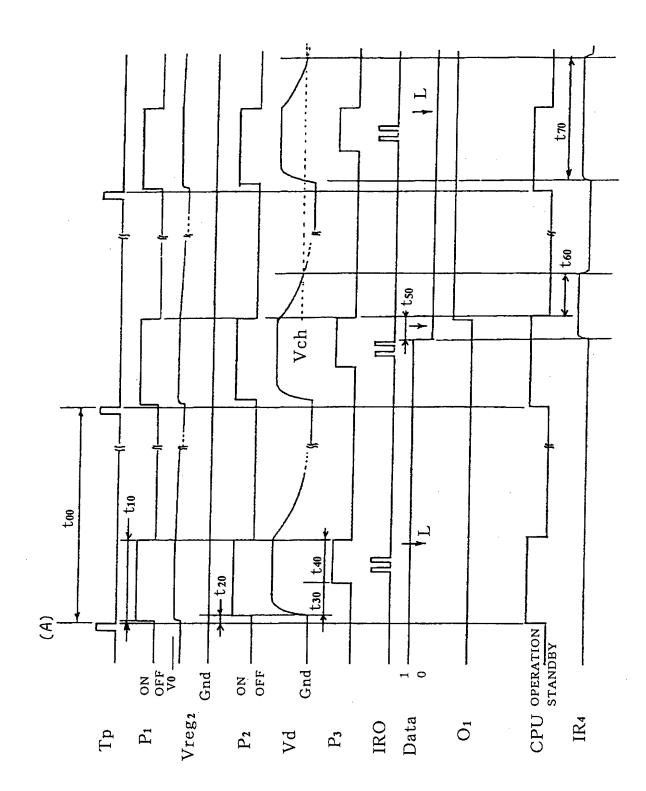


FIG. 5



, cs

t40. (b) tro. t30. FIG. 6 + NARROW t40 (a) t_{L0} t30. O₁ — CPU — IRO – Data - P_3 P_2